



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

JIAN LI, ET AL.

Application No.: 10/762955

Filed: January 21, 2004

For: **LOW-VOLTAGE AND INTERFACE
DAMAGE-FREE POLYMER MEMORY
DEVICE**

Art Group: 2826

Examiner: Thomas L. Dickey

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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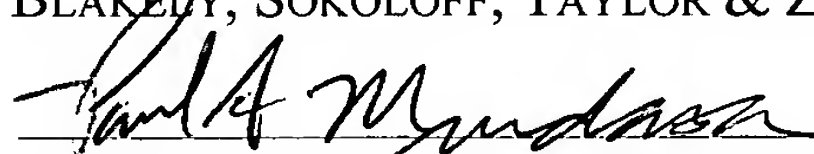
The references were cited in a Search Report dated October 4, 2004 (copy enclosed herewith) in a counterpart PCT application.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Respectfully submitted,

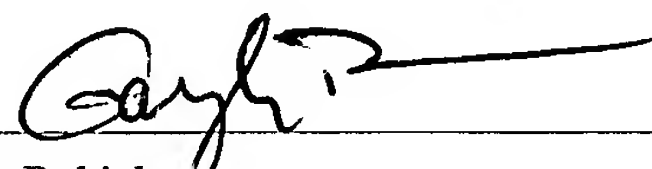
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Date: March 17, 2005


Paul A. Mendonsa, Reg. No. 42,879

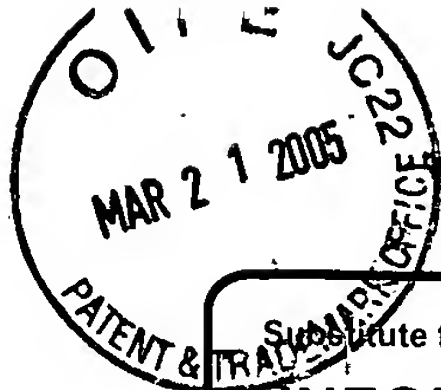
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Complete if Known

Application Number 10/762955

Filing Date January 21, 2004

First Named Inventor Jian Li

Art Unit 2826

Examiner Name Thomas L. Dickey

Attorney Docket Number 42P10047C

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
	1	US-6606261	08-12-2003	GUDESEN et al	
	2	US-6587250	07-01-2003	ARMGARTH et al	
	3	US-6570440	05-27-2003	CHOW et al	
	4	US-6541869	04-01-2003	GUDESEN et al	
	5	US-5932965	08-03-1999	BERGGREN et al	
	6	US-6005707	12-21-1999	BERGGREN et al	
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	10	US-6064615	05-16-2000	GUDESEN	
	11	US-6084850	07-04-2000	GUDESEN et al	
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	13	US-6219160	04-17-2001	NORDAL et al	
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	15	US-6326936	12-04-2001	INGANAS et al	
	16	US-6380553	04-30-2002	GUDESEN et al	
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	18	US-6403396	06-11-2002	GUDESEN et al	
	19	US-6424553	07-23-2002	BERGGREN et al	
	20	US-6429457	08-06-2002	BERGGREN et al	

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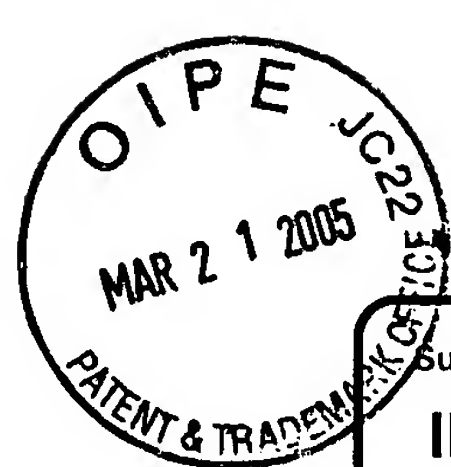
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First Named Inventor

Jian Li

Art Unit

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Thomas L. Dickey

Attorney Docket Number

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		Number - Kind Code ² (if known)			
	21	US-6432739	08-13-2002	GUDESEN et al	
	22	US-6466473	10-15-2002	NAIR et al	
	23	US-6498744	12-24-2002	GUDESEN et al	
	24	US-6498746	12-24-2002	CHOW	
	25	US-6522568	02-18-2003	NAIR	
	26	US-6524887	02-25-2003	LI et al	
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	30	US-6642069	11-04-2003	ARMGARTH et al	
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				Art Unit	2826
				Examiner Name	Thomas L. Dickey
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	41	US-6842357	01-11-2005	BROWN	
	42	US-6858862	02-22-2005	LI et al	
	43	US-2001/0040828	11-15-2001	BERGGREN et al	
	44	US-20020024835	02-28-2002	THOMPSON et al	
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Sheet 4 of 6				First Named Inventor	Jian Li
				Art Unit	2826
				Examiner Name	Thomas L. Dickey
				Attorney Docket Number	42P10047C

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		Number - Kind Code ² (if known)			
	61	US-20030120859	06-26-2003	CHOW	
	62	US-20030120964	06-26-2003	CHOW et al	
	63	US-20030120965	06-26-2003	DAHL	
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	78	US-20040102054	05-27-2004	LEESON et al	
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				Art Unit	2826
				Examiner Name	Thomas L. Dickey
				Attorney Docket Number	42P10047C

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
	81	DIANA et al - US Patent Application 10/816,260 Docket Number P18426 ION IMPLANTING CONDUCTIVE ELECTRODES OF POLYMER MEMORIES		
	82	ANDIDEH et al - US Patent Application 10/712,205 Docket Number P15533 PARALLEL ELECTRODE MEMORY		
	83	DIANA et al - US Patent Application 10/746,173 Docket Number P18097 POLYMER/METAL INTERFACE WITH MULTILAYERED DIFFUSION BARRIER		
	84	STEGER et al - US Patent Application 10/882,423 Docket Number P19508 CHEMICAL-MECHANICAL POST-ETCH REMOVAL OF PHOTORESIST IN POLYMER MEMORY FABRICATION		
	87	COULSON et al - US Patent Application 10/877,914 Docket Number P19081 TEMPERATURE ADAPTIVE FERRO-ELECTRIC MEMORY ACCESS PARAMETERS		

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